

Replacement Sheet

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viability(%)	conc (uM)			
Control		4	40	400
	100	100	100	100
	100	100	96	100
	96	100	100	100
	100	100	100	100
ave	99	100	99	100
stddev	2	0	2	0
test to control		0.391002218	1	0.391002

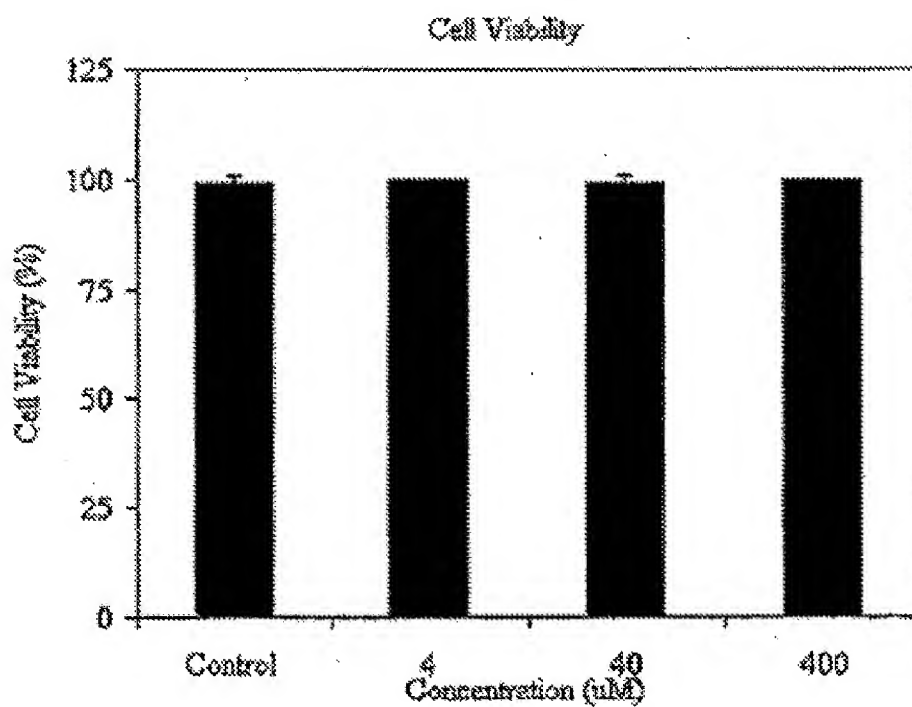


Fig. 12